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by

#### William McMichael Shepherd

Submitted to the Department of Ocean Engineering on 12 May 1978 in partial fulfillment of the requirements for the Degree of Ocean Engineer and the Degree of Master of Science in Mechanical Engineering.

#### **ABSTRACT**

A simulator was designed to model the operation of a single shaft gas turbine engine. The engine characteristics were represented by a third order non linear mathematical model implemented with analog computation. The use of "state of the art" integrated circuitry allowed for a considerable reduction in the space and power required for this device compared with conventional analog methods. A hard-wired desk-top machine was fabricated to simulate the real time dynamic behavior of a gas turbine prime mover.

Thesis Supervisor: Henry M. Paynter

Title: Professor of Mechanical Engineering

Thesis Reader: A. Douglas Carmichael

Title: Professor of Power Engineering

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THE SIMULATION OF GAS TURBINES BY A STATE OF THE ART ANALOG DEVICE

by

WILLIAM MCMICHAEL SHEPHERD

B.S.A.E., U.S. Naval Academy (1971)

SUBMITTED IN PARTIAL FULFILLMENT
OF THE REQUIREMENTS FOR THE
DEGREE OF
OCEAN ENGINEER
AND THE DEGREE OF
MASTER OF SCIENCE IN MECHANICAL ENGINEERING

AT THE

MASSACHUSETTS INSTITUTE OF TECHNOLOGY May 1978

© William McMichael Shepherd

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## I. INTRODUCTION

#### A. BACKGROUND

This paper describes the concept and construction of a hard wired analog device, pictured below, which simulates the operation of a gas turbine engine.



Recent work on gas turbine simulation has focused largely on digital or hybrid computing. To accommodate the nonlinear elements of a gas turbine simulation in this fashion requires a perturbation or piecewise linear technique. A pure analog approach offers a continuous

solution to these nonlinear characteristics, which facilitates a simulation in real time.

At this time, analog computation of complex nonlinear systems is not feasible on devices such as the EAI
680 computer currently installed in the M.I.T. Joint
Computer Facility. With the miniaturization of both digital
and linear functions into the now-common integrated circuit
or IC chip, it becomes possible to design extremely compact
analog devices. The IC component cost of such a simulation
is rapidly approaching the cost of purely passive electronic
elements.

The modeling approach to the analog simulation, physical modeling, follows the work of Markunas<sup>1</sup> closely. Physical modeling attempts to explain the engine characteristics within a framework of physical princples and laws which describe the functions occurring in the real machine. Simplicity is generally a strong point of the model.

The model which has been implemented in the analog simulation represents a general machine "invented" by Markunas, who estimated the necessary characteristic relations between the independent variables. Markunas conducted digital and hybrid simulations of his "general" gas turbine, but a full analog implementation was not investigated.

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This paper is essentially a design manual which traces the development of a hard-wired analog simulator. It is intended to give the reader a brief overview of the analog devices currently available, and their application to a computational circuit. To facilitate future work with the simulator, a detailed user's manual has been written.

This document contains explicit details relating to the construction and operation of the simulator. The user's manual, the simulator, and associated hardware are under the supervisor of Professor Henry M. Paynter, Department of Mechanical Engineering.

### B. DESIGN CONCEPT

The following characteristics were desired in the hard-wired simulator:

- Real time, non-linear third order simulation of the dynamics of a single shaft gas turbine engine, from start-up to overload conditions.
- 2. Cost to be under \$600.
- Size of the device such as to be readily portable and convenient to set-up and operate.
- 4. Layout and operation of the simulator to be comparable with typical industrial

and a some

and military engine control consoles.

- 5. Accuracy of the steady-state simulation to be considered of secondary importance compared with the representation of large scale transient effects.
- 6. The ability to investigate various fuel control schemes in a closed loop control, and the ability to operate with different torque-speed load characteristics.
- 7. Utilization of the general purpose, low cost IC operational amplifier in a hard wired configuration to accomplish all mathematical operations.

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#### II. MODELING

#### A. GENERAL APPROACH

The gas turbine model selected by Markunas will be briefly explained. The modeling approach is broken down into: steady state and dimensional considerations, frequency analysis, and dynamic equations.

Markunas' estimated performance parameters and the dynamic equations are combined in state-variable format. These equations appear in Appendix II and form the basis for the analog signal flow used in the design.

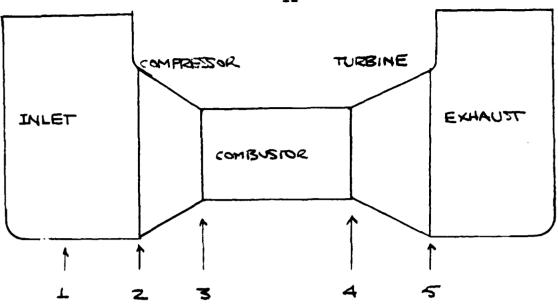
#### B. STEADY STATE AND DIMENSIONAL ANALYSIS

Horlock<sup>3</sup> establishes non-dimensional functions in describing the performance of compressors and turbines.

A set of relations are developed to describe the performance of a turbomachine in its simplest terms.

In the subsequent development, gas turbine state points are used, numbered in the following convention, with total values indicated by a zero in front of the state point:

with mounts



The following symbols are used for dimensional analysis in the compressor:

Symbol	Compressor	Dimensions
Poz	Inlet stagnation pressure	M/LT2
602	Inlet stagnation density	M/L3
Pos	Outlet stagnation pressure	MILTZ
Toz	Inlet stagnation temperature	8
4	Efficiency	*****
D	A characteristic linear dimension	!
ATO	Stagnation temperature rise through compressor	e
7	Rotational speed	1/T
m	Mass flow rate of gas	M1/T

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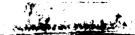
Symbol	Compressor	Dimensions
M	Molecular wt of gas	
u	Absolute viscosity of gas	M/LT
CP	Specific heat at constant pressure	L2/T20
૪	Ratio of specific heats	~
R	Gas constant	L2/T20
902	Speed of sound of entering gas	L/T
T	Torque input	MLZ/TZ

In the compressor, if Pos, M, T and A To are chosen as variables of interest, one can write

Pos, M,  $T_{\perp}$ ,  $\Delta T_0 = f(Poz, Toz, N, D, m, Poz, X, M, Qoz)$ Assuming a perfect gas, fixing the gas constant, R, choosing V, and Toz thus constrains Qoz. Poz can be expressed in terms of R, Poz, Toz; the original relation becomes Pos, M,  $T_{\perp}$ ,  $\Delta T_0 = f(Poz, Toz, N, D, m, R, X, M)$ Taking non-dimensional groups,

With constant values, R and & can be eliminated:

Multiplying the last two non-dimensional parameters to create a new parameter,



It can be seen that the third parameter, is a "flow Reynolds number".

Assuming that the compressor operates above some critical Reynolds number, and that the dimensions of a particular machine are fixed, the equations reduce to:

In order to present meaningful performance relations, compressor data should be corrected following the above parameters.

Identically for a turbine,  $^4$  it can be shown that the same relations hold and that the parameters of interest in turbine performance  $^3$  are m, m, Tz,  $\Delta To$ . All inlet conditions assume values at state point 4, and all exit values are at state point 5. Torque across the turbine is Tz. As shown before,

The characteristics for a given turbomachine can now be specified as functions of dimensionless or pseudo-dimensionless groups:



#### Compressor:

$$\frac{To3}{To2} = f\left(\frac{Po3}{Po2}, \frac{N}{\sqrt{To2}}\right)$$

$$\frac{71}{Poz} = + \left(\frac{Po3}{Poz}, \frac{N}{V Toz}\right)$$

## Turbine:

$$\frac{m\sqrt{ToA}}{PoA} = f\left(\frac{PoS}{PoA}, \frac{N}{\sqrt{ToA}}\right)$$

$$\frac{Tos}{To4} = f\left(\frac{Ros}{Po4}, \frac{N}{\sqrt{To4}}\right)$$

$$\frac{T_2}{R_{04}} = f\left(\frac{P_{05}}{I_{04}}, \frac{N}{\sqrt{T_{04}}}\right)$$



#### C. FREQUENCY ANALYSIS

Modeling of the processes which take place in a gas turbine can generally be classified as mechanical, fluid, or thermal in nature. To reduce the scope of the problem to a more manageable field, only the most significant energy domains and dynamics were considered. Markunas defines three dynamic regimes for the gas turbine:

- 1. Low frequency, (f  $\approx$  .1 hz) where rotary inertias and thermal capacitances dominate the dynamics.
- Medium frequency (f ≈ 1 hz) where rotary inertias and fluid capacitances dominate.
- 3. High frequency ( $f \approx 100 \text{ hz}$ ) where the fluid dynamics are dominant.

Markunas established that rotor dynamics and fluid capacitances were the primary influences on gas turbine dynamics. Thermal capacitances and fluid inertias were neglected.

#### D. DYNAMIC EQUATIONS

#### 1. Assumptions

The gas turbine model is assumed to operate with a perfect gas in one-dimensional flow. Components are treated in lumped parameter fashion.

## 2. Rotor Inertia

For the rotor, a summation of torques give the following:

where

NG = angular acceleration, rpm/sec

 $\Gamma \approx = \text{rotary inertia, ft-lbf-sec}^2$ 

= load, compressor, or turbine torque, ft-lbf

## 3. Fluid Capacitance

For the one-dimensional lumped parameter fluid, the following symbology is used:

p = fluid density, lbm/ft<sup>3</sup>

= mean axial velocity of fluid, ft/sec

= ratio of specific heats = CP/CV

= specific heat at constant pressure

Cv = specific heat at constant volume

Mach number = √x/a

CA = Local sonic velocity = √9c 8RT

= internal energy of the fluid in ft-lbf/lbm

= enthalpy of the fluid in ft-lbf/lbm

- lumped fluid volume



A general mass conservation equation for the one dimensional fluid stream may be written:

Integrating over a constant area duct, with fluid properties constant at a given axial distance or dimension:

$$\frac{2A}{8x} = 0$$
 by definition.

Integrating over the length of the duct

is approximated by an average density,

Lumped fluid volume (Vol.) since

white more thanks

where the subscipts 1 and 2 identify inlet and outlet conditions across the engine, respectively.

Writing a similar equation for the conservation of momentum neglecting viscous shear and body forces,

Integrating over area,

Integrating over length of the duct

$$\frac{\partial}{\partial r} \left( \frac{|P \vee x A|}{3c} \right) dx + \frac{|P \vee x A|}{3c} + \frac{|P A|}{3c} = 0$$

$$\frac{\partial}{\partial r} \left( \frac{\dot{m}}{3c} \right) = \frac{|P A|}{3c} + \frac{|P \vee x A|}{3c} + \frac{|P A|}{3c} = 0$$

$$\frac{\partial}{\partial r} \left( \frac{\dot{m}}{3c} \right) = \frac{|P A|}{3c} + \frac{|P \vee x A|}{3c} = 0$$

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$$\frac{\partial}{\partial r} \left( \frac{\dot{m}}{3c} \right) = 0$$

$$\frac{\partial}{\partial$$



$$M^{2} = \frac{\sqrt{x^{2}}}{\sqrt{3}} = \frac{\sqrt{x^{2}}}{\sqrt{9}(8RT)}$$

$$\frac{d}{dt}(\vec{m}) = \left[\frac{9cA}{L}\right]\left[P(1+8M^{2})\right]_{z}^{L}$$

Since total pressure,  $R = P(1 + \frac{x-1}{z}M^2)^{\frac{\alpha}{\alpha-1}}$ 

$$\frac{d(\vec{m})}{dt} = \left[\frac{g_c A}{L}\right] \left[\frac{R(1+YM^2)}{(1+\frac{y-L}{2}M^2)^{\frac{1}{2}y-L}}\right]^{\frac{1}{2}}$$

Assuming Y = 1.4, and  $M \leq .5$ , the momentum equation can be written:

Similarly for energy conservation with no external work or heat addition,

Integrating over area and length,



where

Since

Substituting,

$$0 + \frac{50}{4} = C \cdot L \left( T + R \left( R - T \right) W_{S} \right)$$

$$\frac{d}{d+} \left[ \left( \frac{1}{6} C \sqrt{L_0} \right) \frac{1 + 8(8-1)M^2}{2} \right] = \frac{CP}{VOL} \left( \frac{1}{M_1} \sqrt{L_0} - \frac{1}{M_2} \sqrt{L_0} \right)$$

where  $T_0 = T + \left(1 + \frac{\delta-1}{2}M^2\right)$ 

With  $M \leq .5$  the result can be simplified to:

## E. STATE EQUATIONS

The three conservation equations are now:

1. Mass

Land on regular

2. Momentum

3. Energy

Using the definition of a polytropic process, PV = const, it follows that

$$P_0 = (const) f_0$$

$$T_0 = (const) P_0 = (const) f_0$$

In order to get the mass and energy equations into more convenient form,

$$\frac{d(R)}{dt} = \frac{(\text{const})(n)}{dt} \frac{d}{dt} \frac{d}{dt}$$

$$\frac{d}{dt} \frac{d}{dt} = \frac{1}{nRTO} \frac{d}{dt} \frac{d}{$$

- deple in consider

With the result that the equations now be more:

$$\frac{d}{dt}(R) = \frac{\overline{NRTo}}{VoL}(\dot{m}_L - \dot{m}_Z)$$

$$\frac{d}{dt}(\dot{m}) = \frac{\overline{QcA}}{L}(Ro_L - Ro_Z)$$

$$\frac{d}{dt}(To) = \frac{n-L}{n}(\frac{c_PRTo}{c_VR_OVoL})(\dot{m}_LTo_L - \dot{m}_ZTo_Z)$$

Where 1 and 2 represent inlet and outlet across the duct. Because of lumped parameter considerations, machine model parameters can be substituted as follows:

One Dimensional	Machine Model
m.	์ พิธ
Μz	m4
To	Toq
Tol	Tos
Toz	Tod
Po	Po3

Substituting notation for the first and third differential equations,

$$Po_3 = \left(\frac{nRTo4}{VOL}\right) \left[ \dot{m}_3 - \dot{m}_4 \right]$$

Land to the state of

$$To4 = \left(\frac{n-1}{n}\right) \frac{\sqrt{RTo4}}{\sqrt{Po3}} \left[\dot{m}_3 To_3 + \dot{m}_F \left(\frac{h_{LHV}}{CP}\right) - \dot{m}_4 To_4\right]$$

Introducing the torque equation as the third system equation,

$$N_G = \left(\frac{30}{\pi I}\right) \left[T_2 - T_L - T_3\right]$$

where

LHV = fuel lower heating value, Btu/lbm

m = fuel flow, lbm/sec

T = load torque, ft-lbf

P3, T64, AND N6 are chosen as variables of convenience.

For an isentropic process, n = 1.4. Markunas felt that n-1/n should be replaced by 1.0 in the temperature equation.

## F. ENGINE DATA, ASSUMPTIONS, AND CHARACTERISTICS

## 1. Introduction

In Markunas' analysis a single shaft machine was invented from a number of sources. Several real world machines exist which have design parameters close to

make so grande at

Markunas's engine. The recent Garrett GTPF 990 engine in its locked shaft mode is a reasonable example.

## 2. Design Point Specifications

Pressure Ratio	5.0
Compressor Isentropic Efficiency	1280°F
Turbine Isentropic Efficiency	88%
Compressor Discharge Mass Flow	100 lbm/sec
Inlet Pressure	14.175 psia
Inlet Temperature	80°F
Rotor Speed	7200 rpm
Output	6948 HP

Markunas plotted characteristic curves for his compressor and turbine, which appear in Appendix I.

Markunas then mathematically "fit" each characteristic curve. These approximations follow the functional relations established for non-dimensional and pseudo dimensionless groups.

## 3. Compressor and Turbine Characteristics

Markunas defined the following variables and characteristics for his compressor:

PRC - PO3/POZ

pressure ratio

and the manual of a

temperature ratio

scaled compressor mass flow

scaled compressor speed

$$(2000 = 200)$$

and our makes

GEC = 
$$\frac{8}{8}$$
  
 $\times WC = AC \left[ 1 - \left( \frac{PRC - 1}{BC} \right) \right]$   
 $EFFC = DC \left[ 1 - \left( \frac{EC - PRC}{EC - L} \right) \right]$   
 $TRC = 1 + \left[ \frac{PRC}{EFFC} \right]$ 

Similarly, for the turbine

PRT = 
$$\frac{Pos}{Ro4}$$

TRT -  $\frac{Tos}{Tod}$ 
 $\frac{m_4\sqrt{Tod}}{pod}$ 
 $\frac{m_4\sqrt{$ 

and the said translate to

$$CT = .7$$

$$DT = .88[1 - (1 - XNT)^{4}]$$

$$ET = 1 . - .8(XNT)^{1.25}$$

$$GET = \frac{x-1}{8}$$

$$XMT = AT[1 - (PRT)^{BT}]^{CT}$$

$$EFFT = 1 - [1 - \sqrt{\frac{1-ET}{1-PRT}}]^{2}$$

$$TRT = 1 - EFFT[1 - (PRT)^{GET}]$$

#### Additional Machine Assumptions

The machine characteristics are combined with the differential equations to produce a set of system state equations. The expanded equations appear in Appendix II.

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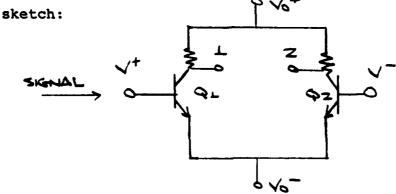
## III. THE OPERATIONAL AMPLIFIER

#### A. INTRODUCTION

The fundamentals of the operational amplifier, (OA) are briefly presented to establish the necessary analog "tools" to proceed to a simulator design.

## B. BASIC OPERATION

An ideal OP amp can be modeled by the following  $9 \checkmark 6^+$ 



with no differential voltage between positive  $(\bigvee^+)$  and negative  $(\bigvee^-)$  inputs, both emitters are equally biased, and collector currents  $\bigcirc_L$  and  $\bigcirc_Z$  are equal. The differential voltage output between 1 and 2 is also zero. Driving  $\bigvee^+$  more positive with a signal will cause the current through  $\bigcirc_L$  to increase. The current through  $\bigcirc_L$  must decrease by a proportional amount, so that in the differential voltage output between 1 and 2, 1 gets driven more negative with respect to its initial voltage, and 2 becomes more positive.

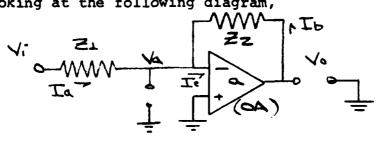
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Thus for a small signal applied to one of the input terminals, a high voltage gain and current "buffering" takes place as a result of the transistor characteristics.

With the ideal OA, the input current is essentially zero, and the voltage gain is assumed infinite.

#### c. THE INVERTING OA

Looking at the following diagram,



It is assumed that the impedance of the amplifier Zc > 0, therefore Ic 20.

Closed Loop Gain, from a summation of currents:

$$Ib = (V_0 - V_0)/2z$$

$$Ia = (V_i - V_0)/2z ; Ia = -Ib$$

$$V_0/2z + V_0/2z = V_i/2z - V_0/2z$$

$$V_0 - -a = (Z_1/(Z_1 + Z_2))$$

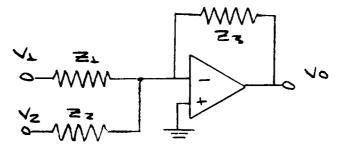
$$V_1 + [aZ_1/(Z_1 + Z_2)]$$

If a, the amplifier gain, is large,

It can therefore be seen that the inverting OA inverts and provides a gain proportional to the input and the signal feedback resistances.

## D. INVERTING SUMMER

By joining multiple input currents to the input side of the OA, an inverting summer can be constructed with variable gains on each input.

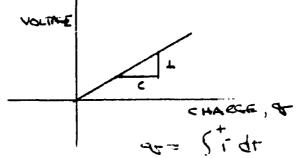


By current summation,

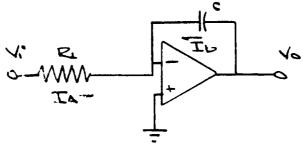
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## E. INTEGRATOR

Adding a capacitor to the feed back loop of the OA adds the following constitutive relation to the feedback loop:



Looking at a typical circuit,



$$I_{A} = -I_{b}$$

$$\Delta V_{0} = I_{b}$$

$$\Delta V_{0} = -\frac{V_{i}}{R_{i}C} \Delta t$$

$$V_{0} = -\frac{1}{R_{i}C} \left( V_{i} \right) dt$$

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#### F. LOGARITHMIC GENERATOR

Using a diode in the feedback loop enables the logarithmic nature of the diode voltage - current relation to be included in the loop. The diode characteristic can be modeled by the following equation:

where:

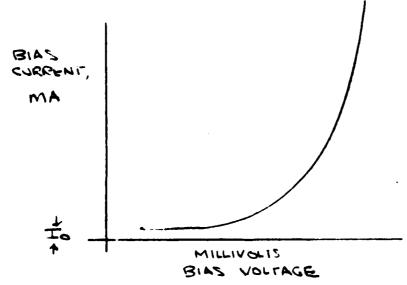
To = junction saturation current

Vo = junction saturation voltage

T = bias current

✓ = bias voltage

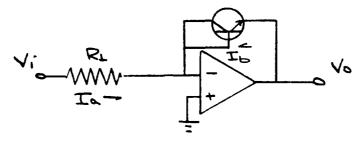
A typical characteristic appears as:



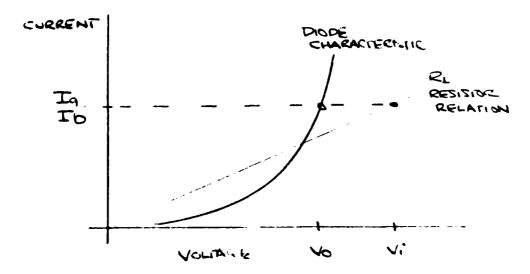
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It is important to note that the saturation voltage  $\sqrt{o}$  is directly proportional to the absolute temperature.

A typical log circuit can be constructed as follows:

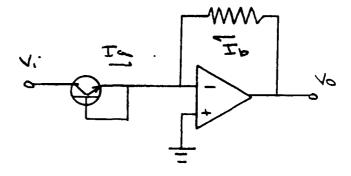


A diode - connected transistor is used to perform the diode function, as transistor emitter-base junctions have more dynamic current range than diodes in general. By the following diagram, an input  $\bigvee_{i}$  across  $\bigvee_{i}$  causes a current flow through the "transdiode". The bias voltage, appearing as  $\bigvee_{i}$  varies as the log of  $\bigvee_{i}$ .



## G. ANTILOG GENERATOR

In a similar fashion, input voltages or currents may be exponentiated in the following circuit:



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#### IV. ANALOG COMPUTATION

#### A. INTRODUCTION

To implement the non-linear system equations the following functional operations are performed: addition, subtraction, integration, multiplication, division, exponentiation to a fixed power, and exponentiation to a variable power. The first three analog operations have been briefly discussed. To implement the higher functions, the use of logarithmic circuits must be made.

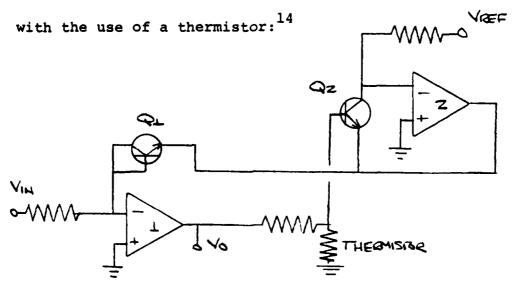
#### B. THE PRACTICAL LOG CIRCUIT

There are at least three major considerations in the construction of a practical log or antilog circuit: cost, complexity, and accuracy. Three different architectures are considered to accomplish the log function.

The first choice is the previously discussed simple log/antilog circuit. Using low quality components, cost of the log circuit is under one dollar. Thermal stability is considered poor. A test circuit and transdiode characteristics are shown in Appendix III. Test data indicated that the accuracy and thermal properties of this circuit were marginal.

A second choice is a compensated log circuit as diagrammed below, which offers increased thermal stability

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In the diagram, the collector of  $\mathfrak{P}_2$  is kept at virtual ground by the OP amp, and the collector current through  $\mathfrak{P}_2$  is equal to a constant reference current provided by  $\mathfrak{P}_2$ . It can be shown that the output voltage is of the form  $\mathfrak{P}_2 = -\mathsf{P}_2$  ( $\mathfrak{T}_2$ ) where the thermistor provides temperature compensation. One of the drawbacks to this scheme is that the number of components necessary to generate the log function has gone from 3 to at least 9, with proportional space and cost increases. This configuration was not evaluated.

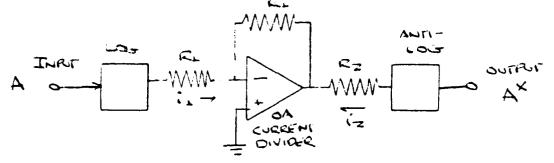
The third and most expensive choice is to utilize a dedicated logarithmic device. The particular unit tested was a Texas Instrument log amplifier. This is a dual log generator on a single DIP package, at a cost of

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approximately \$2.50. With appropriate circuit manipulation, the desired log/anti-log operations can be performed. The TI log amplifier was evaluated in depth. Results are documented in Appendix III. The log amplifier was the foundation of the simulator design. Other dedicated log/antilog modules exist which offer increased dynamic range, temperature stability, and accuracy, but alternative devices were an order of magnitude more costly than the TI log amplifier.

#### C. EXPONENTIATION - ALL CASES

In order to achieve either fixed or variable power exponentiation, the use of logarithms is basic. The signal flow for a fixed power exponent (power function generator or PFG) circuit is diagrammed below:



By a judicious selection of values for  $\mathbb{R}_1$  and  $\mathbb{R}_2$  current can be multiplied or divided using an operational amplifier in the ratio:

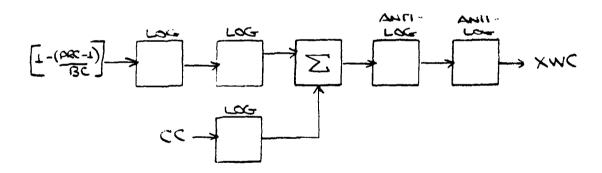
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To be able to exponentiate to a variable power requires a cascaded log/antilog arrangement. The use of a segment of the state equations is made to demonstrate the concept. It is desired to generate the following:

$$XMC = K(T - (\frac{BC}{BC} - T))_{CC}$$

where Krepresents the scaling of several fixed parameters, and XWC, RRC, BC and CC are all problem variables.

The circuit selected must process:



As a result of thermal instabilities, the interior section of the signal flow was replaced with a four-quadrant multiplier.

The multiplier essentially compresses the separate functions into a single chip. By circuit manipulation,

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a multiplier can also divide, square, and take square roots. A range of multiplier devices currently exist, with the price of the least expensive about \$3.

Test data on the multiplier used in the simulator was collected in Appendix III. To allow as compact a circuit as possible, the multiplier was frequently utilized in its dividing and exponentiating modes.

Complete device specifications are included in Appendix VI.

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#### V. PRELIMINARY DESIGN

#### A. INTRODUCTION

The simulator design proceeded in a parallel fashion. In an iterative process, estimations were generated for the electronic layout, which allowed the cabinet and control panel to be sized and designed.

The simulator is configured to resemble a real-world engine control panel. Additional concepts for the peripheral design which were not incorporated were:

- Proportional fuel control schemes with variable gains.
- Torque-speed loads characteristic of pumps, propellers, generators, and other real world devices.
- Overspeed alarms and stall audio noise circuits.

#### B. ELECTRONICS

#### 1. Logic Circuit

The logic processing of the electronic circuitry was broken down into 3 separate grid structures, which appear as design drawings in Appendix V. Use was made of bread-board panels to avoid the inflexibility of other techniques. Concurrent with testing various mathematical

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operations, the circuit design was modified to balance the competing parameters of simplicity, size, accuracy, and cost. The appropriate equation variables were normalized about their design point values, and then scaled to provide a circuit signal that was approximately 75% of the maximum rates input for the device. The steady state design point values for all variables were established by a DYSYS program and used for the normalization.

Additional notes on the wiring techniques and breadboard layout are included in Appendix VI.

#### 2. DYSYS Simulation and Scaling

A DYSYS simulation of the system equations was run as it appears in Appendix IV. Significant comments on the program are:

- 1. The conditional value of FC was changed to a fixed value of 3.0 to facilitate the circuit logic.
- 2. Markunas estimated a parabolic "pump-type" load torque, which appears as .0000977(3)\*\*2.
- 3. The modeling parameter DT is called TT in the program to avoid confusion with the DYSYS time-step variables, DT.

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The scaling procedure started with the following listings:

#### a. Constants

Poz, Pos	2 0 00 1 0 0	ZOAL LBF/FFZ
Toz	<b>©</b> 181 0.7	539.7 12
<b>୪</b>		1.4
R	<del>"</del> -q	53.4 Fr-LBI /LEM'K
Cp	·	SAT BLO YORW- K
hlhv	ings . Gast	16444 BW/18W

### b. Machine Characteristics

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c. Input Variables at Design Point

d. State Variables at Design Point

The DYSYS simulation was run to verify Markunas's simulations. Use was made of the program to assist in adding voltage and current values to the circuit design, and to evaluate the effect of fixing the discontinuous constant, FC. FC was conditionally assigned the value of 2 or 4 in Markunas work. The steady state effect of assigning FC as 3 was investigated and adopted.

The DYSYS simulation was also run off - design to generate a test case for the logic circuit. The simulation was run at approximately 50% fuel flow and steady-state values were generated for all elements of the equations.

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Some difficulty was encountered in determining the correct value for the lumped parameter fluid volume. The value which appeared to be consistent with Markunas' data was 144 ft<sup>3</sup>. An approximation based on real world data gave 15 ft<sup>3</sup> as a more likely figure. Since the volume represents a time constant scaling for the fluid capacitance, it has no significance for the DYSYS steady state values. Changes in the volume assumed can be readily investigated in the simulator dynamic response.

The scaled voltage values and the system variables they represent are drawn on the 3 circuit plans in Appendix V.

#### C. PERIPHERALS

The main elements of the remaining design work included the chasis, power supply and distribution, instrumentation and control hardware, and breadboard architecture. The design drawings and sketches included in Appendix V illustrate the final peripheral design. The photograph below shows the internal layout of the simulator.

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Appendix VI, specifications, contains additional data on the peripheral equipment, its wiring, and operating parameters.

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#### VI. IMPLEMENTATION AND CHECKOUT

#### A. PROCEDURE

To establish the characteristics of the various devices and to investigate the methodology of the design, 10 tests were conducted. The significant results are discussed here in the context of the design process. All test data is included in Appendix III.

#### B. LOG GENERATION

Tests 1 and 2 determined the voltage - current relations for the transdiode - connected OP amp, using both 2N2222 and 2N2904 transistors. It became apparent that the allowable voltage values of the log circuit were so narrow that only current variations could be used for the signal, over about a 1 1/2 decade range of values.

#### C. POWER EXPONENTIATION

Tests 3, 4, 5, and 6 were designed to varify the fixed power exponentiation circuitry. A workable configuration was wired in test 6, using a current - dividing resistive network.

Tests 7, 8, 9, and 10 were conducted to validate
the variable - power exponentiation procedure. The inability
of the transdiode - connected OP amp to provide a stable

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cascaded log circuit led to the investigation of a 4 quadrant multiplier in Test 8.

The Exar 2208 multiplier characteristics were investigated to generate hookup diagrams, trimming values, and allowable voltage values.

With Test 9, a simple log/multiplier/simple antilog circuit was wired and observed. Stability was significantly improved, but was still considered below the accuracy level desired.

In Test 10, a Texas Instrument 441 log amplifier was tested to determine its log/antilog characteristics. The log-amp was incorporated into the variable exponentiation circuit and the stability evaluated. The steady state accuracy of the log amplifier in the variable exponentiation circuit was somewhat better than the simple log configuration. Since the dedicated log amplifier offered better compensation characteristics than the simple log configuration, it was substituted at all points in the circuit design.

#### D. GENERAL CIRCUIT DESIGN

Concurrent with the test sequence, the overall circuit design was changing as new test data was generated. The more significant problems which caused iteration of the circuit are briefly discussed. One immediate limitation

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exists using the TI log amplifier module. Looking at "TL 441 Transfer Characteristics" included in Appendix III, the log amplifier only has a useable output range of 0 - 450 millivolts. This can be doubled by wiring an OP amp to the input side of the log amplifier to use the dual input capability of the device. Still, the output accuracy is limited, since various offsets for multipliers and OP amps can easily reach 10 millivolts.

As a general rule, it was decided to trin the circuit at a limited number of points, rather than dealing with the offsets required for each component.

It was intended in the final circuit design to operate all components at one dual-polarity supply voltage. This choice nominally would be  $\pm$  8 volts, the maximum rating of the most sensitive device, the TI log amp. Test 10 demonstrated that significant nonlinearities were introduced into the multiplier characteristics if the supply voltage were reduced. Three solutions were postulated:

- 1. Run the multipliers at  $\pm$  15 volts, and the OP and log amps at  $\pm$  8 volts.
- 2. Compensate the multiplier to have linear characteristics at + 8 volts supply.
- Replace the multiplier modules with log amplifiers to process the same function.

The first choice was selected in an effort to keep the circuit design as simple as possible, in as much as the single - voltage supply was more of a packaging consideration than an electronic one.

#### VII. CONCLUSIONS AND RECOMMENDATIONS

#### A. FEASIBILITY OF THE DESIGN

From the test data collected, a real time simulation can be conducted with the logic circuits wired as designed. The techniques developed for the implementation of non-linear differential equations should be applicable to a wide range of system models.

Markunas defined his engine in essentially non dimensional or pseudo-dimensionless groups. It was not possible to investigate the ability of the simulator to represent different geometric machines, however, it is felt that future work will verify the utility of the simulator and the nonlinear model.

#### B. DESIGN REFINEMENTS

The simulator as designed could be significantly enhanced with more general torque and fuel control schemes. If the simulator could demonstate sufficient generality in modeling the behavior of real world gas turbines, an autonomous "observer" circuit could be configured for a wide range of engines. This observer device, using the logic developed in the simulator, could be an extremely compact unit. It could provide improved control of fuel, speed, temperature, and pressure dynamics. Such a circuit could be incorporated in one

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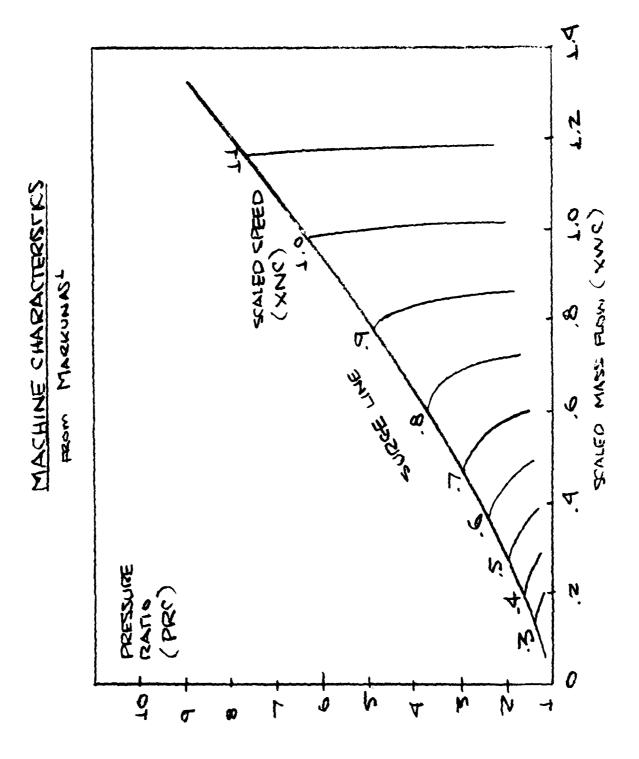
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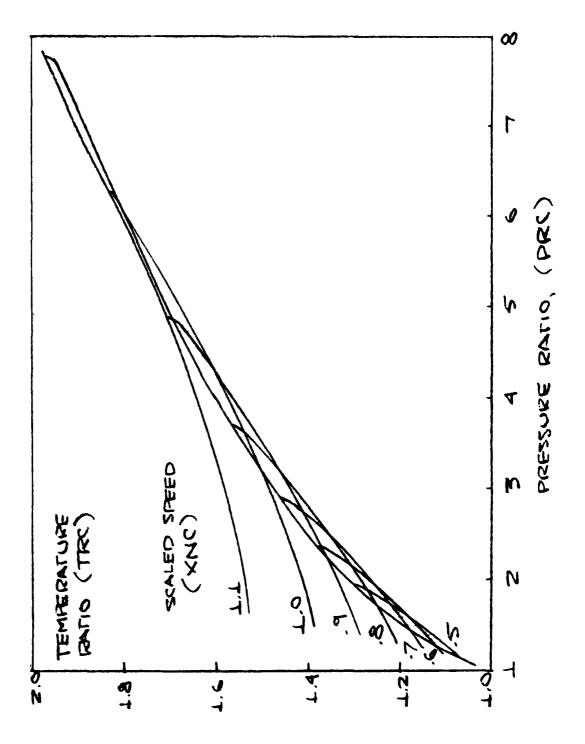
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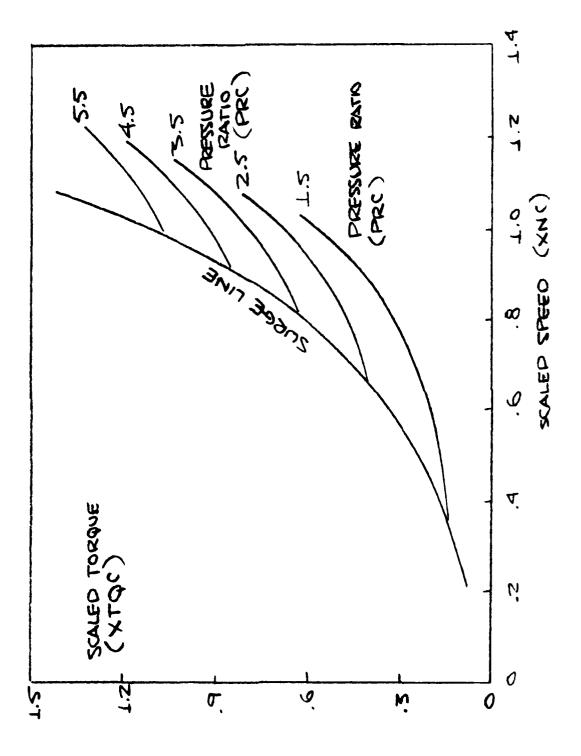
## APPENDIX I MACHINE CHARACTERISTICS

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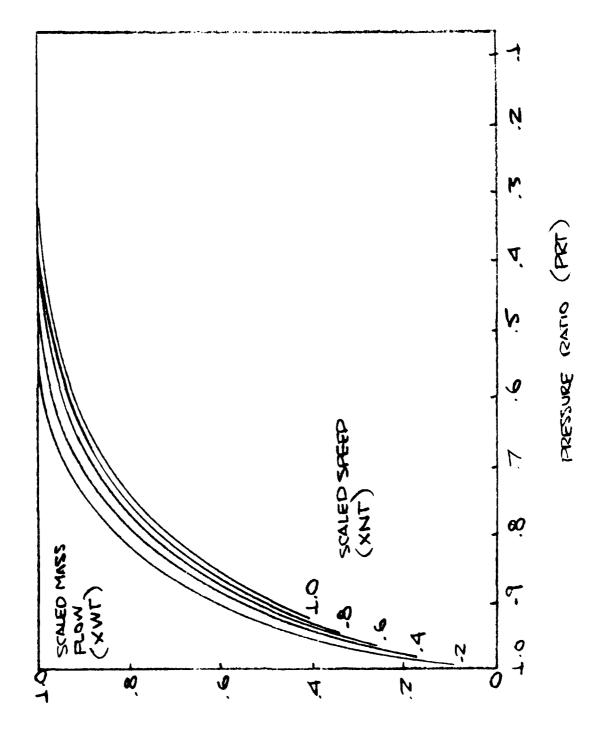




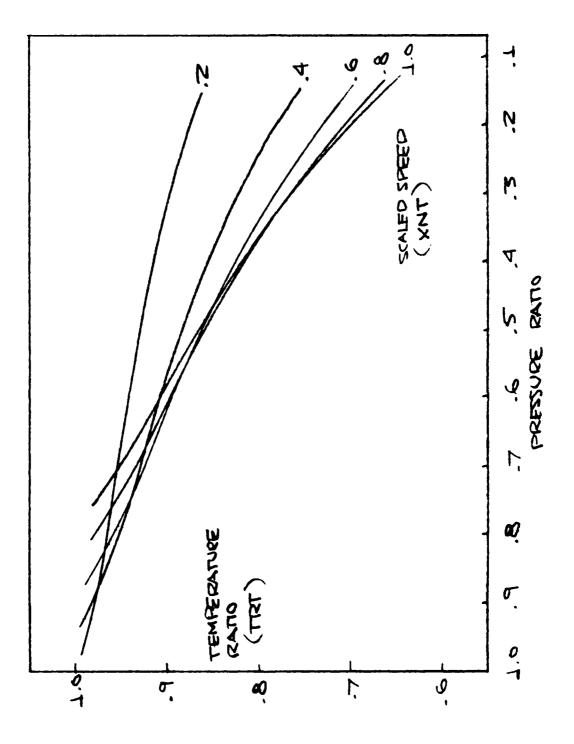
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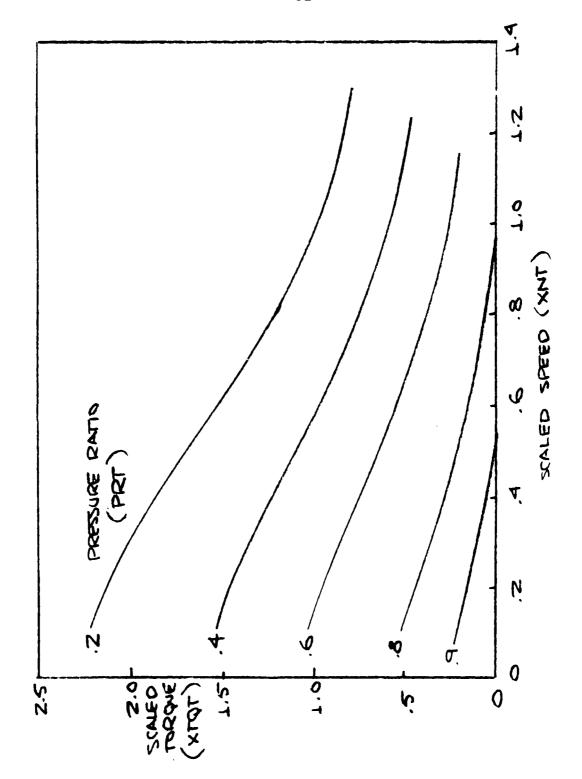
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# APPENDIX II STATE EQUATIONS

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(nRTG) (XMC)(AC)(XWC) - (XMT)(AT)(XWT) (nRTGA) m3 - m4 Pos =

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No = 720 [72 - 12 - 12]
No = 720 [72 (xmr)(A)

30 30CP [53 (xMT)(AT)(1-PRTBT)(2-(1-(1-1-8(xNr)-25- 3 ) [(1-(PRT) ]-(XMC)(AC) (XWC)(TOZ)(TRC-L)

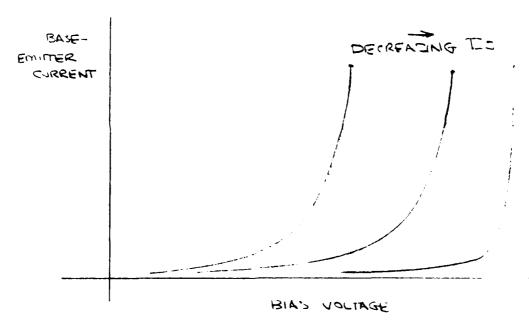
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#### APPENDIX III

#### Test Data

#### A. TEST 1 AND 2

From reference data, <sup>19</sup> the transdiode voltage current characteristics was estimated for various values of saturation current. The log circuits diagrammed subsequently for the 2N2222 and 2N2904 transistors demonstrated low base-emitter saturation currents, thus making the exponential characteristic curve more abrupt.

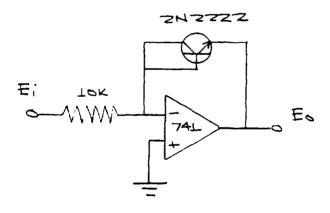


## Test Data

$$I = I_s e^{(E_O/26Mv)}$$

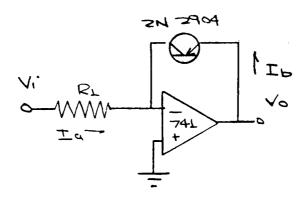
E <sub>O</sub> (Mv)	I (MA)	Estimated IS (PA)	
470	.004	$5.04 \times 10^{-11}$	
548	.015	$1.05 \times 10^{-11}$	
581	.045	$8.87 \times 10^{-12}$	
609	.121	$8.13 \times 10^{-12}$	
616	.178	$9.14 \times 10^{-12}$	

## Test Circuit



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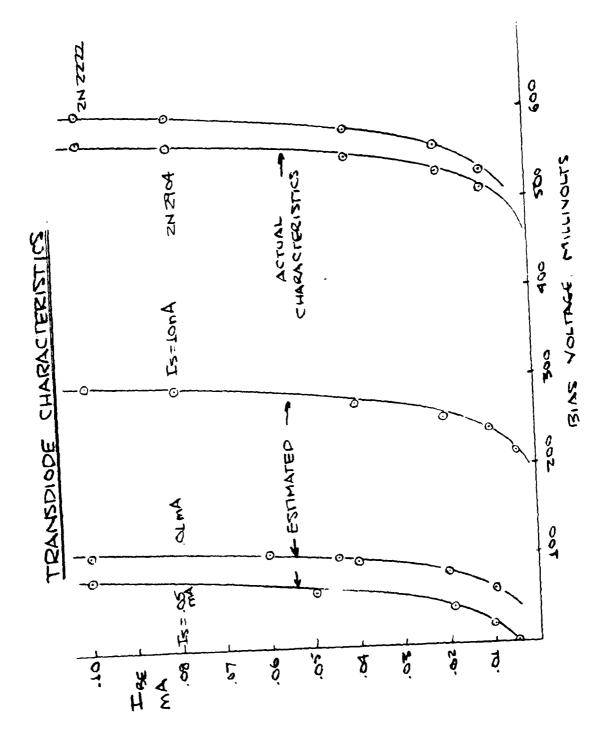
TEST 2
CIRCUIT DIAGRAMS AND DATA



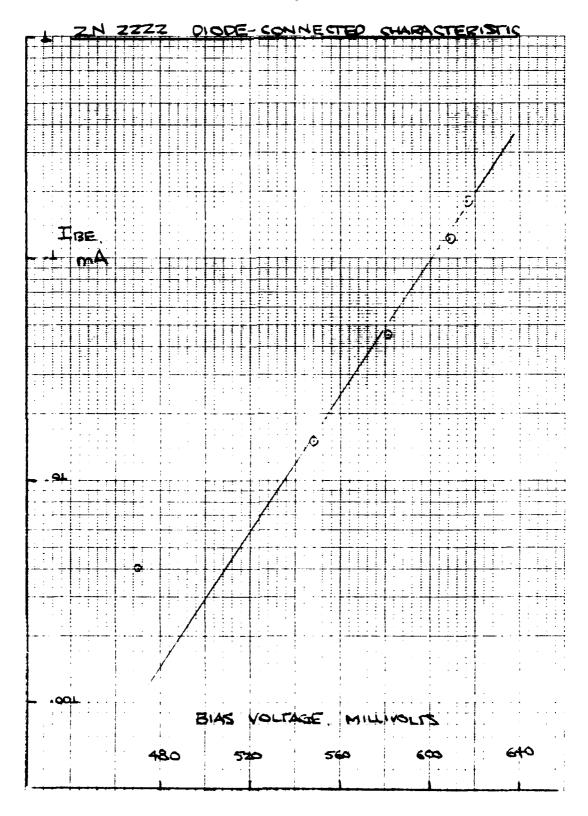
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Vi (var.)	Ic (mA)	(mV)	InnA
-4.82	161	530	- <u>1</u> - 1 - 1
-1.32		٠٠٠ <u>٠</u> ٠	<b>-15</b> €
- 3.95	133	553	.133
· 2.12	.070	కాట	M Co.
1.71	.057	559	. 265
BT ZMILCHED LD	30K		
003		384	
110		423	
021		445	
044		465	
O7L		476	
106		487	
142		552	
-,2-51		509	
-,354		SII	
130		233	

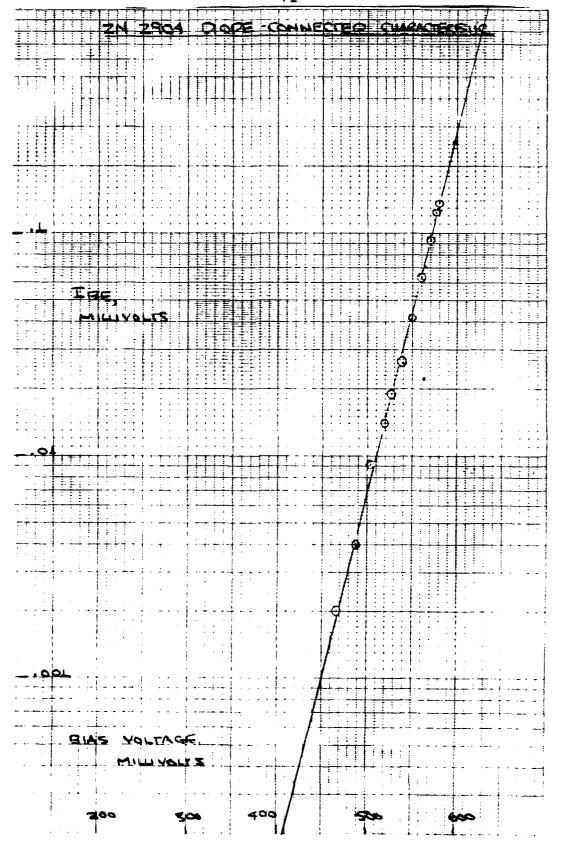
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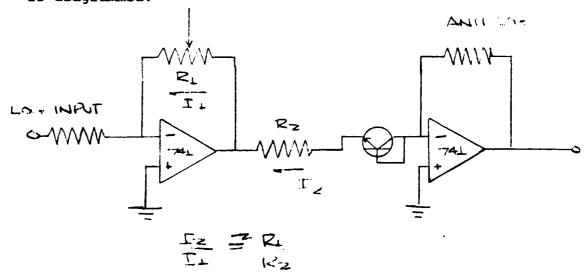
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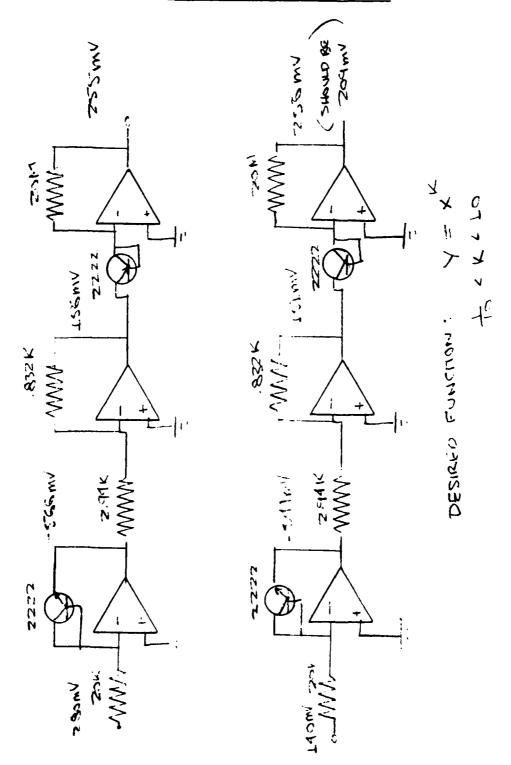
## B. TESTS 3 - 6

The fixed exponentiation of a variable was evaluated in Tests 3 - 6. Tests 3, 4, and 5 were failures as the essential concept of current division to achieve the exponentiation was not understood. Test 6 demonstrates the use of a resistive network which accomplished the current manipulation. It is now apparent that as a result of the characteristics of the simple log configuration, current division can be easily accomplished by assigning the appropriate resistor value to the antilog input. This is diagrammed:

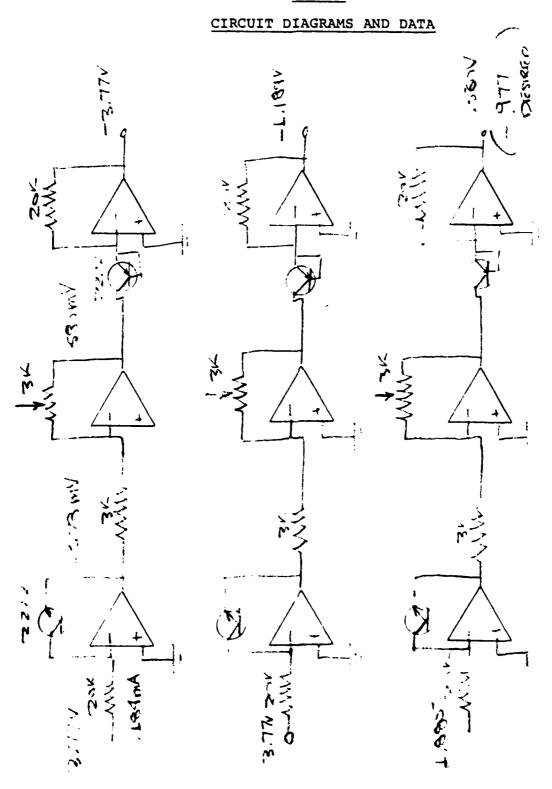


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TEST 3
CIRCUIT DIAGRAMS AND DATA

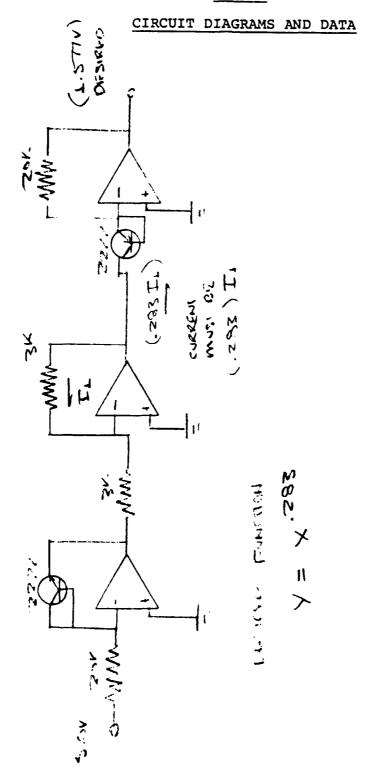


TEST 4



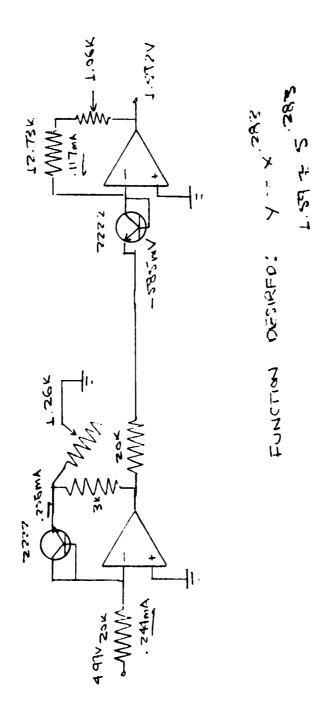
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TEST 5



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TEST 6
CIRCUIT DIAGRAMS AND DATA



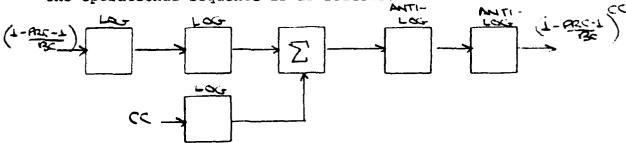
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## C. TEST 7

It is desired to validate the circuit segment for the operation

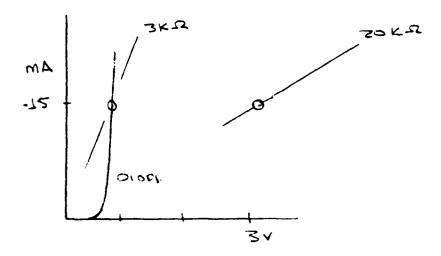
$$\left(1-\left(\frac{PRC-1}{13C}\right)\right)^{CC}$$

The operational sequence is as follows:



The circuit described in the test data was set up to evaluate the feasibility of the operation.

The desired operating characteristic of the cascaded logger - antilogger is picutred as follows:



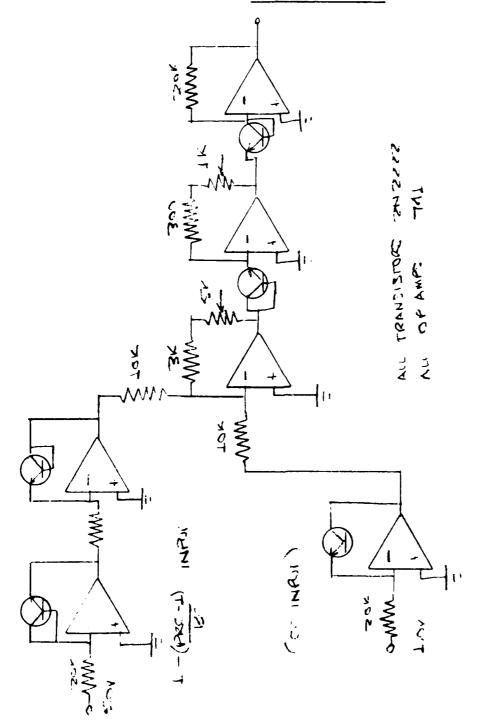
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The first log geneator establishes a voltage - current relation based on the diode and 20K resistor.

Consider the second log generator to be superimposed in the first with a 3K resistor and a second diode.

The cascaded antilog - configured amplifiers
displayed gross thermal sensitivity. The simple OP amp transdiode configuration is unsuitable for this cascaded
operation without additional temerature compensation.

TEST 7
CIRCUIT DIAGRAM

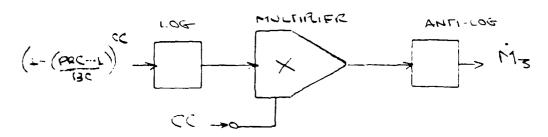


### D. TEST 8

## i. Purpose

Test 8 was conducted to verify the variable exponent part of the analog circuitry, using the four quadrant multiplier.

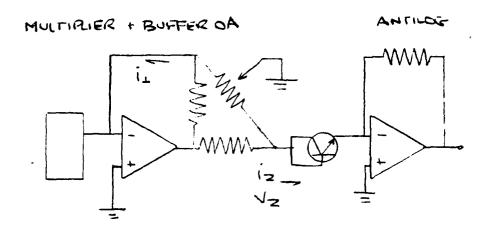
The equation of interest is written below:



## ii. Methodology

The functional method used to exponentiate to a variable power was to regulate the multiplier output current in the ratio called for by the exponentiation. For example:

For multiplier scaling the integral buffer OP amp in the 2208 was used for the current division with a resistive bridge as follows,



such that  $i_1 = i_2$  and the output voltage of the multiplier is thus controlling the current input to the antilog amplifier transdiode.

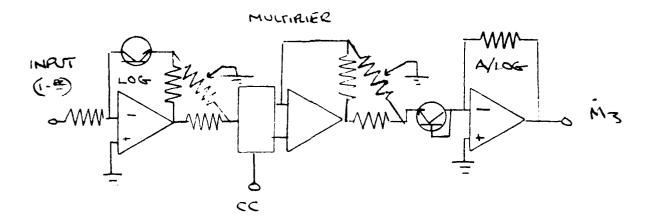
The multiplier output is nominally:

VZ = 
$$\frac{\sqrt{x}\sqrt{y}}{10}$$

IN OBTAIN VZ & 600 MV, RESIDENCE
VALUES MUST BE SELECTED TO GET THE
DESIRED RANGE OF AUTHOR IMPORTS.

It was determined that a resistive bridge must be used for the multiplier input from logging OA, sketched

as follows:



so by modulation of CC input, the desired current ratios across multiplier can be achieved.

# iii. <u>Data</u>

A 5 minute observation of the steady state output voltage was made to investigate the circuit thermal stability.

The second second

# 10 Second Samples

# -Volts x 10

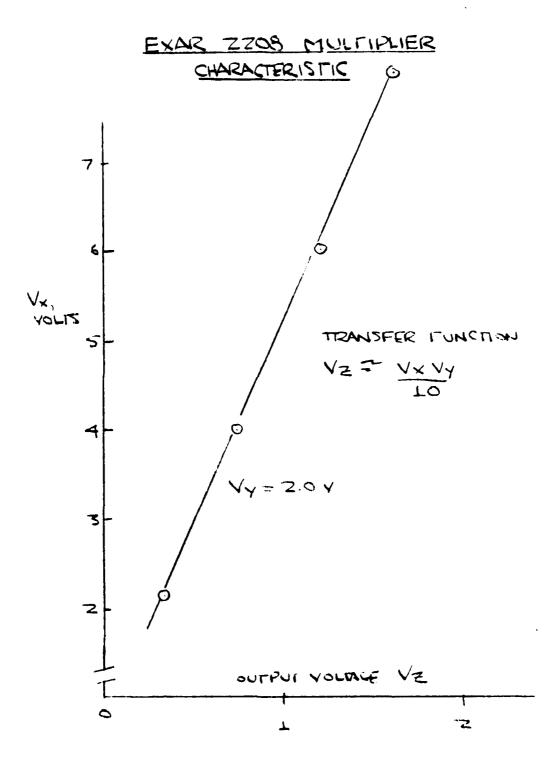
-630	.650	639
. 635	.643	.634
. 625	.677	.654
- 667	.649	,640
. 644	. 651	.642
. 850 ·	.6-31	.629
.630	120.	.631
.624	.691	,626
.66L	,652	

# 26 Samples

MEAN = . 642

STO OFVIATION = . OLZT

The second second second



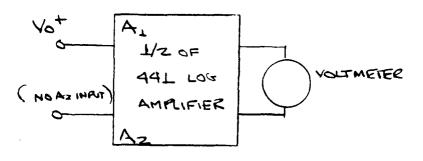
matches in the committee to

# E. TEST 9

# i. Purpose

To investigate the Texas Instrument TL 441 log amplifier.

# ii. Log Configuration



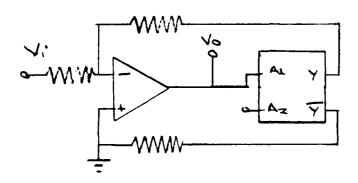
v <sub>o</sub>	$\underline{v}_{\underline{Y}\overline{\underline{Y}}}$	$\frac{v_o}{\sqrt{y\bar{y}}}$
		( VIRTUALLY IDEILITICAL
.002	.028	FOR IMPUT AT PIN
, o10	.060	A > )
.070	,097	, , ,
. 050	.143	
.080.	.251	
.L00	, <del>2</del> 75	
.150	.313	
15:2	. <del>74</del> 3	
.300	. <del>-37</del> 9	
<i>,5,0⇔</i>	.416	
1.003	.423	
2.00	.427	

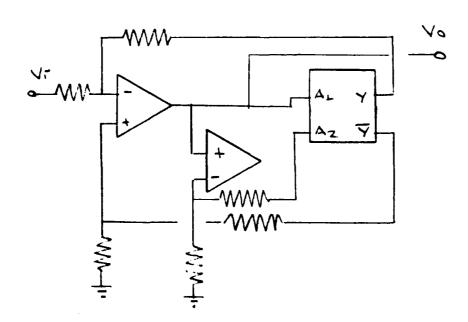
make in morning

The output characteristic  $\sqrt{\gamma} = \sum_{i} \sum_{j} A_{i} + i \sum_{j} A_{j}$  verified. From the plot, "TL 441 transfer characteristics, the log amplifier has 1 1/2 decades of dynamic input range, from 20 - 600 mV.

# iii. Antilog Configuration

TWO ALTERNATE WIRINGS WERE INVESTIGATED:





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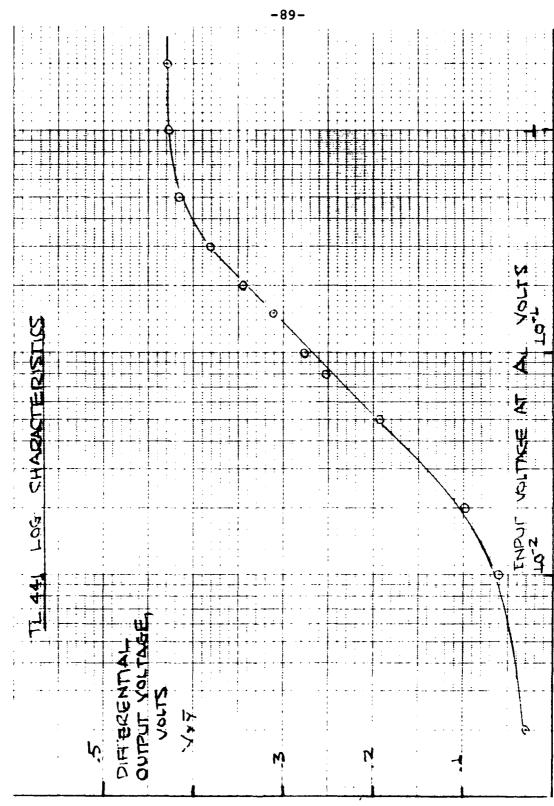
# Antilog Configuration 1

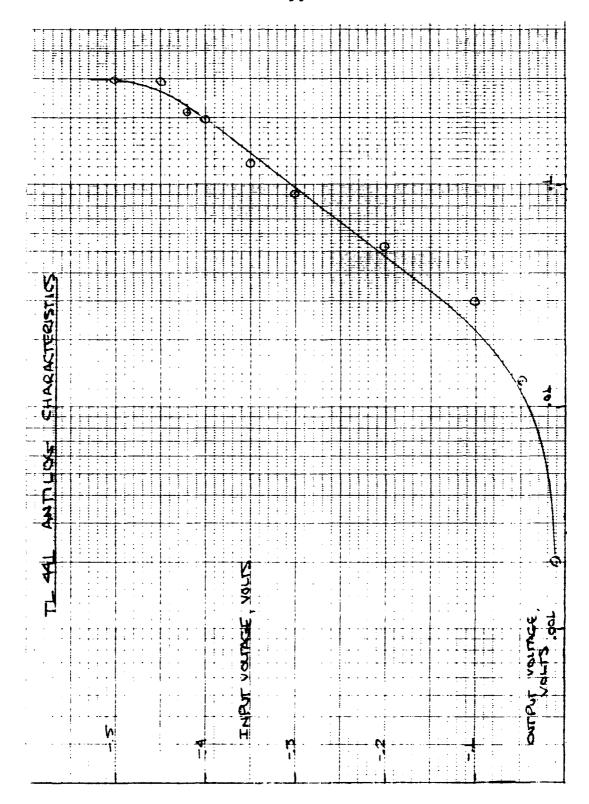
<u>v</u> i	<u>v</u> o	V <sub>o</sub> + .028
100	- 10 cB	ÿ
010	- , 026	.002
049	-,018	
100	- , 002	.~ 50
200	. 625	.05'\$
···.,300	,063	<b>۱۱</b>
-,400	.171	.rad
500	,295	(.296) SATURATION
600	.296	<b>34</b> 102111011
JOF	.296	
120		
<i>ae1.</i> .	NO DATA	
·. 180		
- 350	E10.	123
450	,293	( .Z43 ) SANDEARCH
- ,420	.213	. 241

which is market

The antilog data is graphed in "TL 441 antilog characteristics" for configuration 1. Configuration 2 will expand the antilog dynamic range somewhat above the 1 1/2 decades available for configuration 1.

marin or want



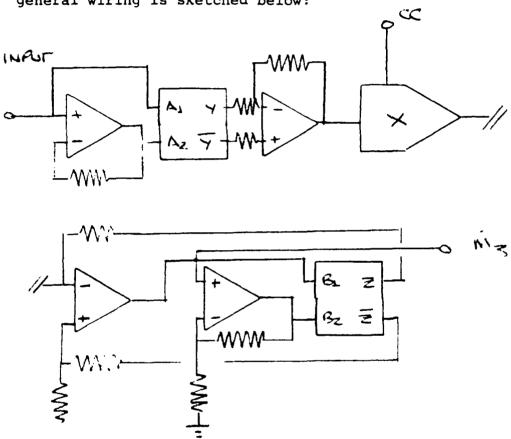


and the second

## F. TEST 10

## i. Purpose

To incorporate the 441 log amplifier and the quadrant multiplier in the variable exponent circuit. The general wiring is sketched below:



## ii. Steady State Performance

The circuit demonstrated the ability to track a desired variable exponent. It was of primary interest to determine if the thermal properties were improved over the simple log-fed multiplier circuit.

Land of the

## The following data was generated:

# Variable Exponent Test

1.50	7.33	7.42
1.57	47	1.37
1.55	1 A7	1.46
± 47	1.42	T +1
4.55	L 47	1.53
T.25	1.54	47 - 47
1.45	4.45	1.43
7.25	1.41	
7 20	1.46	
		(10cm)

## 25 Samples

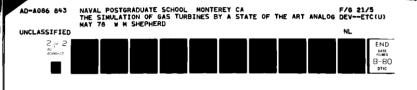
MEAN = 1 474 VOLTS

This represents a 50% reduction in the drift over the simple log configuration. Since the 441 log amplifier is fairly cheap, it is substituted for the simple log configuration.

# APPENDIX IV

DYSYS SIMULATION

```
- mp*(1.-(1.-copen//1.-rm)/(1.-spm)))*(1.-spen((1.-sm)/(1.-
                   COMMON D.DT. Y(2'), P(2'), STIMI, PTIME, NEWDT, TEMPT, N
                                                                                                                                                                                                                                                                                                                                                                                                         ((6*-DRX)*(0*-DNA)+6*)LdUD*9088* = DD
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        EFFC = DC*(1.-((SC-DPC)/(RC-1.))**FC)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         CT = .7
TT = .8P*(1.-(1.-(NT)**44.)**.65
                                                                                                                                                                                                                                                                                                                                                                                                                                                                      CC = .01803*(2.-XMC)**5.794
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            Tec = 1. +( PPC**CPC-1.) /9PFC
                                                                                                                                                                                                                                                                                                                                                                                                                                                   E. 1 1 . + YYC+ 3.64 YKC++U.1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     PT = 3.1 + 1.62/(// + .24)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ToT = 1.-EFFT*(1.-Port*GFT)
                                                                                                                                                                                                                                                                                                                           (C)*C()*(n1)*405)/5h = 488
                                                                                                                                                                                                                                                                                                                                                AC = .75*YNC+.27*YNC**3.0
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   V Д П В В П Ф ( 1 ° − Г Е П + в Г П ) + в С П
                                                            IF (NEWDY.ME.-1) 70 TO 1
                                                                                                                        (
                                                                                                                      1 IS(NEADT.EG.C) GT TO
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                E7= 1.-.8*Y4T**1.25
                                                                                                                                                                                                      Y(5) = 03/16320.
Aluca chidibation
                                                                                                                                                                                                                         y(6) = T4/21F9.
                                                                                                                                                                                                                                                                PAC = P3/2041.2
                                                                                                                                                                                                                                             *(7) = 40/8000.
                                                                                                                                                                                                                                                                                                         ANC = NG/120C.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   しき人 ** シット モーンと
                                                                                                                                                                                                                                                                                     reT = 1./PRC
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 AT = 1.0006
                                                                            GFC = .2831
GFT = .2536
                                                                                                                                          52 = \frac{1}{2}(1)
Thu = \frac{1}{2}(2)
                                                                                                                                                                                  36 = 7(3)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            1604)))
                                                                                                                                                                                                                                                                                                                                                                                                                               .
.
                                      54 2874
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        ;---
(k
(j
(k
```



```
IF(T.LT.FFTIME-1.) GO TO 2
PUT PPC.FPT.XVC.XVT.AC.PC.CC.DC.FC.XWC.FFFC.TPC.AT.BT.CT.TT.FT.
184T.FFFT.TPT.FT.GOTC.4T.TF.TOC.TQT
                                                                                                                                                                                                                                            E(2) = .06P4*(UC*TC+70225.*Y(4)-WT*Y(2))
E(3) = .3291*(TOT-TOC-.000097*Y(3)**2.)
E(3)
                                                                           TOC = 1740.*WC*(TC-539.)/NG
TOT = 2011.*WT*(TH-TE)/NG
#F = 539.69*PPC
#F = 4096*P3/SOFF(T4)
#R = T4*TRT
                                                                                                                                                                                                                   2 F(1) =800.*(3C-9E)
```

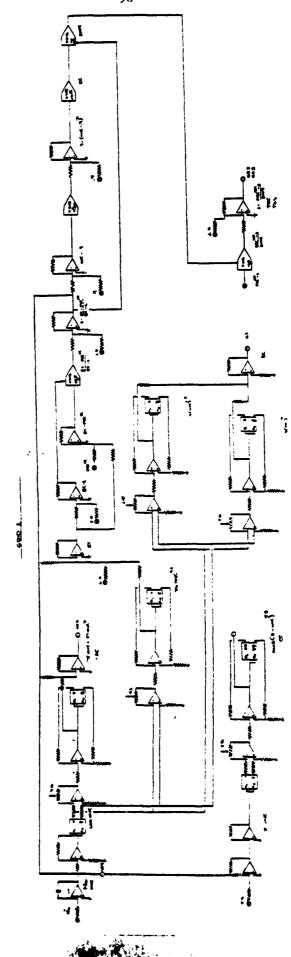
STIME = 0., FILME = 10., TSTEP = .05, W = 3, Y(1) = 10205., Y(2) = 1739.7, Y(3) = 7200., Y(4) = 1.1807 1PR4T=1,2,3, PFWTC=9., 10., 1.6 DYSYS SYSTEM NAMELISY

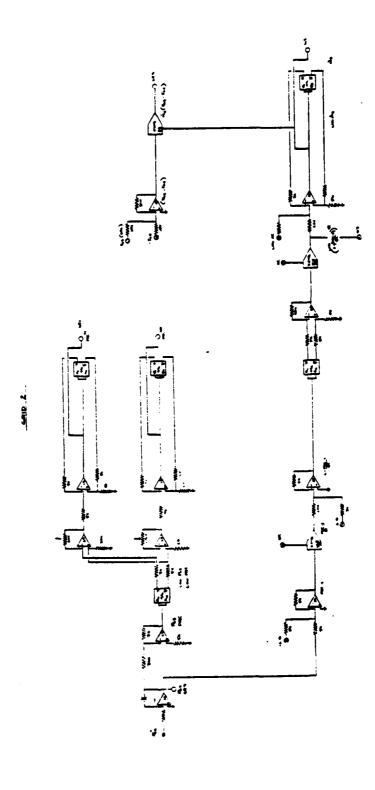
	ا ا	1.50.1			
5.6120	= UM x	1.0013			
4.3850	H FJ	0000L*v			
.98000	= LoL	0.79487			
1226.3	≝ DGE	9312.9			
5.0676	1000	9,19970	X	= 1.00cP	
5.0194	ن ت	1.79508E-02	0	= 0.84012	
.83323	Tec	1.6894	E-1	= 1.0006	
.88050	[r.	r.19926	XWT	= 1.0000	
100.13	10	911.73	E CR	= 100.13	
14349.					
5.00008-02	1.0221E	04 1740.	-	7205.	
3 T U W 1 T T C W	4.3850 0.88000 1725.3 5.0075 5.0194 (.83823 (.89000 100.13 14349.	### 19850 CT = ### 1925.3 TOC = ### 1905.5 FOC = ### 1905.13	TPT = 0 TOC = 0 TOC = 1 TPC = 1 TPC = 1 TPC = 1	GT = 0.70000 TPT = 0.70447 TOC = 0.19470 CC = 1.74508E-02 DC TPC = 1.6894 NT ET = 0.19425 NT TC = 911.73 NT	GT = 0.70000 TPT = 0.70000 TOC = 0.19070 GC = 1.79508E-02 DC TPC = 1.6894 ET = 0.19926 XWT TC = 911.73 HT

THE VEPTABLES	MINIX	1.0221E 64	1741.	7205.	1.181	0.6250	0.8062
STATE AND AUXIL	FINE SELECTION OF	1.0266F 94 1.0221E 64	1740.	7260.	1.191	¢.425¢	9,305,0
TREME VALUES OF			2)	Y( 3)	(7	5)	(¥)
TRE							

# APPENDIX V

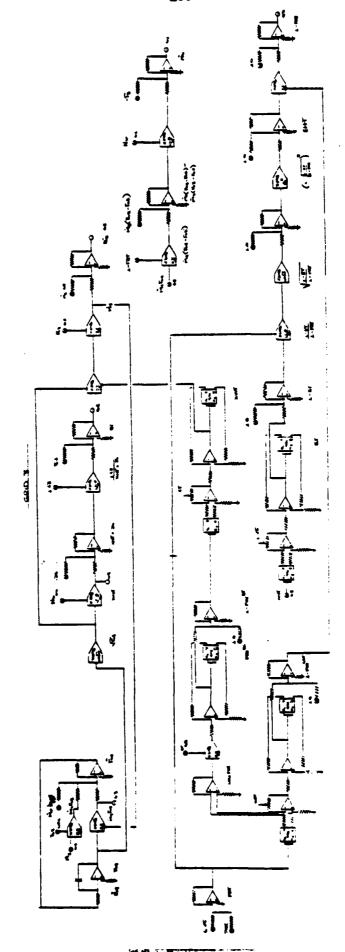
DESIGN DRAWINGS AND SKETCHES





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# SIMULATOR LAYOUT

NA PROPERTY

PAWER SUPPLY 0 POWER DISTRIBUTION 00000 FLEL & BASK METERS PANEL EXTERIOR FUEL

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JUD JOH

#### APPENDIX VI

## **SPECIFICATIONS**

#### A. ELECTRONIC COMPONENTS

# 1. Operational Amplifiers

voltage

voltage

Maximum ratings

## a. Fairchild MA 741

Maximum ratings
supply voltage + 18v

Power dissipation 500 MW

Supply current 2.8 MA

Typical input offset

## b. Fairchild MA 324 Quad OP amp

supply voltage
single polarity

Input voltage range

-0.3v to 32v

Power dissipation

900 MW

Supply current

2 MA

Typical input offset

1 mv

2 mv

## 2. EXAR 2208 Four Quadrant Multiplier

Maximum ratings supply voltage # 18v

Power dissipation 750 MW

Typical supply current 5 MA

Typical output nonlinearity .5%

# 3. Texas Instruments TL 441 Logarithmic Amplifier

Maximum ratings
supply voltage ± 8v

Input voltage 6v

Power dissipation 500 MW

Typical differential output offset voltage 40 Mv

Typical supply current vcc 18.5 MA

vcc 8.5 MA

# 4. Trimming Potentiometers

All applications: Spectrol type 64 ceramic potentiometers,  $1-100 \mbox{K}\Omega$ 

## 5. Resistors

All applications: Corning 2% 1/4 watt resistors

## 6. Power Supply

Analog Devices model 975 dual polarity power supply

Output voltage ± 15v

Maximum rating 500 MA

Typical output error 1%

#### B. PERIPHERALS

## 1. Breadboard Panels

Continential Specialties model QT 59 6.5" panels

# 2. Volt Meters

Beede Instrument 3" panel meters, model 3-03-8

250° movement

 $200\Omega$  internal resistance

# 3. Joystick

2 - axis movement; two,  $100 \text{K}\Omega$  rotary potentiometers on each axis.

## 4. Chassis

.062" clear anodized aluminum assembled with pop-rivet construction.